



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Kouichi IKEDA et al

Art Unit: 2823

S. N. 09/716,843

Examiner: B. Kebede

Filed: November 17, 2000

For: SEMICONDUCTOR DEVICE AND
METHOD FOR MANUFACTURING
THE SAME

RECEIVED
APR 14 2003
TECHNOLOGY CENTER 2800
U.S. PATENT AND TRADEMARK OFFICE

Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated October 4, 2002, for which a 3 month extension of time is requested, to extend the period for response to April 4, 2003, please amend the above-identified application as follows:

In the claims:

Please cancel non-elected claims 1-4, 7 and 10.

Please amend claims 5 and 8 by replacing it with the like-numbered claim hereinbelow. Markups are provided at the end of this response showing the changes made by the amendment.

Al
5. (Amended) A method for manufacturing the semiconductor device, comprising:

a first step of forming a plurality of identical semiconductor chips on a semiconductor wafer;